

#9

FROM Paul N. Katz
DATE March 14, 2002
PAGES 9 (including cover)

VOICE NO. 713.229.1343
FAX NO. 713.229.7743
RETURN TO J. Clements @ 3522

TO Mr. Kevin Little
U.S. Patent Office

FAX NO. 703.305.9822
VOICE NO. 703.308.1863

MESSAGE

RE: U.S.S.N. 09/522,026
Filed: 10/08/1998
First Named Inventor: Fink, et al.

SECOND PRELIMINARY AMENDMENT

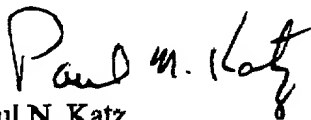
Mr. Little:

Pursuant to our telephone conversation earlier today, following is a copy of the Second Preliminary Amendment previously filed by facsimile transmission for the above-referenced patent application. Included with this facsimile are copies of the two cover pages as verification of the earlier transmissions filed on 02/07/2002 and again on 02/20/2002.

Both transmissions were sent to the attention of Examiner Roy K. Potter, Group Art Unit 2822. Examiner Potter was the Examiner in the parent case. The apparatus claims submitted in this Second Preliminary Amendment are within the realm of this same Group Art Unit 2822.

I appreciate your attention to this matter. Please do not hesitate to contact me if you have any questions or need additional information. Thank you.

Respectfully,



Paul N. Katz
Reg. No. 35,917
ATTORNEY FOR APPLICANTS

HOU02:846152Notice of Confidentiality

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09522026-031402

 *** TX REPORT ***

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ST. TIME	02/20 09:46
USAGE T	02'16
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RESULT	OK

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FROM Paul N. Katz
 DATE February 20, 2002
 PAGES 8 (including cover)

VOICE NO. 713.229.1343
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TO Examiner Roy K. Potter
 Group Art Unit 2822
 U.S. Patent Office

FAX NO. 703.308.2864
 VOICE NO. 703.308.4106

MESSAGE

RE: U.S.S.N. 09/522,026
 Filed: 10/08/1998
 First Named Inventor: Fink, et al.

Please see the following:

"Second Preliminary Amendment Under 37 C.F.R.1.115(a)"

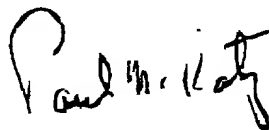
"IDS by Applicant (PTO/SB/08A)"

(hard copy with copies of cited references sent via Express Mail).

Examiner Potter:

Per our telephone conversation this morning, following is a copy of the Second Preliminary Amendment for the above-referenced patent application as it was filed via facsimile on February 7, 2002. Also included is a courtesy copy of our facsimile receipt verifying its transmission on that date.

I appreciate your help with this application. Please do not hesitate to contact me if you have any questions.



Paul N. Katz

Reg. No. 35,917

ATTORNEY FOR APPLICANTS

 *** TX REPORT ***

TRANSMISSION OK

TX/RX NO	0950
CONNECTION TEL	17033082864
CONNECTION ID	USPTO
ST. TIME	02/07 10:27
USAGE T	02'01
PGS. SENT	7
RESULT	OK ✓

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FROM Paul N. Katz
 DATE February 7, 2002
 PAGES 8 (including cover)

VOICE NO. 713.229.1343
 FAX NO. 713.229.7743
 RETURN TO J. Clements @ 3522

TO Examiner Roy K. Potter
 Group Art Unit 2822
 U.S. Patent Office

FAX NO. 703.308.2864
 VOICE NO. 703.308.4106

MESSAGE

RE: U.S.S.N. 09/522,026
 Filed: 10/08/1998
 First Named Inventor: Fink, et al

*Please see the following:**"Second Preliminary Amendment Under 37 C.F.R. 1.115(a)"**"IDS by Applicant (PTO/SB/08A)"**(hard copy with copies of cited references to follow via Express Mail).**Thank you.*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: §
FINK, ET AL. § Group Art Unit: 2822 §
Serial No.: 09/522,026 §
Filed: OCTOBER 8, 1998 § Examiner: POTTER, ROY K. §
Title: AN INTEGRATED CIRCUIT (IC) PACKAGE §
WITH A MICROCONTROLLER HAVING AN §
N-BIT BUS AND UP TO N-PINS COUPLED § Atty. Docket: 068354.0161 §
TO THE MICROCONTROLLER (AS AMENDED) §

CERTIFICATE OF TRANSMISSION
37 C.F.R. 1.8

PURSUANT TO 37 C.F.R. 1.8, I HEREBY CERTIFY THAT I HAVE A REASONABLE BASIS FOR BELIEF THAT THIS CORRESPONDENCE IS BEING SENT VIA FACSIMILE TRANSMISSION TO THE FACSIMILE TELEPHONE NUMBER BELOW, ON THE DATE INDICATED BELOW, AND TO THE ATTENTION OF EXAMINER ROY KARL POTTER.

Paul N. Katz
PAUL N. KATZ REG. No. 35,917

Honorable Commissioner for Patents
BOX NON-FEE AMENDMENT
Washington, D.C. 20231

DATE/TIME OF FACSIMILE TRANSMISSION: 10:30 A.M. CST. 02/07/2002
FACSIMILE NUMBER FOR TRANSMISSION: 703.308.2864
DIRECT TELEPHONE NUMBER: 703.308.4106

SECOND PRELIMINARY AMENDMENT UNDER 37 C.F.R. 1.115(a)

Dear Sir:

Applicants respectfully submit this Second Preliminary Amendment for the above-styled patent application. Pursuant to 37 C.F.R. 1.115(a), this Second Preliminary Amendment is being submitted on or before the mail date of the first Office Action under §1.104, and is therefore considered timely filed. Applicants respectfully request that this Second Preliminary

204780-92022560

Amendment be entered into the file wrapper and used for substantive examination of the claimed invention.

AMENDMENTS

In the Title:

Please enter the following replacement title. A marked up version of this replacement title showing the changes made thereto is attached after this Second Preliminary Amendment.

**An Integrated Circuit (IC) Package With A Microcontroller Having An
N-Bit Bus And Up To N-Pins Coupled To The Microcontroller**

In the Specification:

On page 1, lines 5-7, please enter the following replacement paragraph in the section titled Related Applications. A marked up version showing the changes made thereto is attached after this Second Preliminary Amendment.

RELATED APPLICATION

This application is a continuation of commonly owned U.S. patent application Serial Number 08/644,916, filed May 24, 1996, now United States Patent Number 5,847,450, issued December 8, 1998.

In the Claims:

Please cancel without prejudice claims 11-24 entered from the first Preliminary Amendment. Applicants respectfully request that the following new claims 25-28 be added before substantive examination of the application.

2011-03-14 09:22:56

-25. An integrated circuit (IC) package, comprising:

**an IC chip with a microcontroller therein, the microcontroller having an
n-bit bus; and
up to n-pins electrically coupled to the microcontroller.**

**26. The IC package of claim 25, wherein at least one of the up to n-pins is a
multiple function pin.**

27. An integrated circuit (IC) package, comprising:

**an IC chip with a microcontroller therein, the microcontroller having an
eight-bit bus; and
eight pins electrically coupled to the microcontroller.**

**28. The IC package of claim 27, wherein at least one of the eight pins is a
multiple function pin.—**

REMARKS

Claims 11-24 submitted in the first Preliminary Amendment have been canceled without prejudice and new claims 25-28 have been added for substantive examination.

All amendments are made in a good faith effort to advance the prosecution on the merits. Applicants reserve the right to subsequently take up prosecution on the claims as originally filed in this or appropriate continuation, continuation-in-part and /or divisional applications. No new matter has been added.

09522026-031402

A copy of the Information Disclosure Statement ("IDS") (PTO/SB/08A) accompanies this Amendment. The IDS and copies of all of the cited references are being sent on Thursday, February 7, 2002 via U.S. Postal Service Express Mail Label EV067266141US to:

Honorable Commissioner for Patents
Box IDS
Washington, D.C. 20231
ATTN: Examiner Roy K. Potter, Group Art Unit 2822

SUMMARY

In light of the above amendments, early examination is earnestly solicited. Should the Examiner have any questions, comments or suggestions in furtherance of the prosecution of this application, the Examiner is invited to contact the attorney of record by telephone or facsimile. If there are any fees due with the filing of this Response, including any fees for an extension of time, Applicants respectfully Petition the Commissioner for such an extension and direct that any and all fees be charged to Baker Botts L.L.P., Deposit Account Number 02-0383 (*formerly Baker & Botts, L.L.P.*), Order Number 068354.0161.

Respectfully Submitted,

By: Paul N. Katz

Paul N. Katz

Registration No. 35,917

Baker Botts L.L.P.

910 Louisiana Street

Houston, Texas 77002-4995

Telephone: 713-229-1343

Facsimile: 713-229-7743

ATTORNEY FOR APPLICANTS

February 7, 2002

204760-92022560

AMENDMENT VERSION WITH MARKINGS TO SHOW CHANGES MADE

In the Title:

At page 1, beginning at line 1, the title was amended as follows:

An Integrated Circuit (IC) Package With A Microcontroller Having An N-Bit Data
Bus Width With Less Than N I/O Pins And A Method Therefor
Up To N-Pins Coupled To The Microcontroller

In the Specification:

At page 1, lines 5-7, the following paragraph was amended as follows:

RELATED APPLICATIONS

This application is a continuation of commonly owned ~~Divisional Application of~~ U.S.
patent application Serial Number 08/644,916, filed May 24, 1996, ~~in the name of the same~~
~~inventors as the present patent application~~ now United States Patent Number 5,847,450,
issued December 8, 1998.

204720-9222560